VCortex - STSYS Knowledge transfer materials traceability matrix

| Item | SOW chapter | Description | File Name |
| --- | --- | --- | --- |
| 1 | 6.1.1 | ETHIIC Specification document  with specific version for Stratasys. | ETHIIC Specification ver 1.3.docx |
| 2 | 6.1.2.1 | HW block Diagram | \ETHIIC HW documents\Netlist, electronic scheme (circuit drawing)\ ETHIIC 1.004.DSN |
| 3 | 6.1.2.2 | Net list | \ETHIIC HW documents\Netlist, electronic scheme (circuit drawing)\allegro\ netlist.log |
| 4 | 6.1.2.3 | Electronic Layout | \ETHIIC HW documents\Electronic layout\ ETHIIc.brd |
| 5 | 6.1.2.4 | Electronic Circuit Dwg. | \ETHIIC HW documents\Netlist, electronic scheme (circuit drawing)\ ETHIIC 1.004.DSN |
| 6 | 6.1.2.5 | Mechanic Layout | \ETHIIC HW documents\Mechanical drawing\ Mech\_ETHIIC.pdf |
| 7 | 6.1.2.6 | BOM | \ETHIIC HW documents\Production files\ ETHIIC BOM ver 1.004.xlsx |
| 8 | 6.1.2.7 | Production Files + ATP | \ETHIIC HW documents\Production files |
| 9 | 6.1.3.1 | Burning Instruction document | \ETHIIC SW Documents\ SVD for ETHIIC Firmware (includes burning instructions).doc |
| 10 | 6.1.3.2 | Firmware binary files | \ETHIIC SW Documents\Firmware Binary Files\ ETHIIC 2.11 STSYS 14 NOV 2014.bin |
| 11 | 6.1.3.3 | Firmware SW Design Document | \ETHIIC SW Documents\ ETHIIC Firmware Design Document ver 1.0.docx |
| 12 | 6.1.3.4 | Firmware SW Source code | \ETHIIC SW Documents\Firmware Source Code and build files\ETHIIC |
| 13 | 6.1.3.5 | Firmware SW build files | \ETHIIC SW Documents\Firmware Source Code and build files\ETHIIC |
| 14 | 6.1.3.6 | DLL for the Host Computer Design Document. | \ETHIIC SW Documents\ IO\_extender DLL Design Document ver 1.0.docx |
| 15 | 6.1.3.7 | Source code for the Host Computer DLL | \ETHIIC SW Documents\IO\_Extender DLL Source Code and build files\SW\ETHIIC\_Client\IO\_Extender |
| 16 | 6.1.3.8 | Build files for the Host Computer DLL | \ETHIIC SW Documents\IO\_Extender DLL Source Code and build files\SW\ETHIIC\_Client\IO\_Extender |
| 17 | 6.1.3.9 | Testing Simulator Design Document | \ETHIIC SW Documents\ETHIIC Testing Simulator Design Document.docx |
| 18 | 6.1.3.10 | Testing Simulator Source code | \ETHIIC SW Documents\IO\_Extender DLL Source Code and build files\SW\ETHIIC\_Client\ETHIIC\_Client |
| 19 | 6.1.3.11 | Testing Simulator build files | \ETHIIC SW Documents\IO\_Extender DLL Source Code and build files\SW\ETHIIC\_Client\ETHIIC\_Client |
| 20 | 6.1.4.1 | Programming & burning SW | \ETHIIC SW Documents\LMFlashProgrammer\_1613\ LMFlashProgrammer.msi |
| 21 | 6.1.4.2 | Programming & burning HW | Physically attached – VCortex ETHIIC programmer |

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